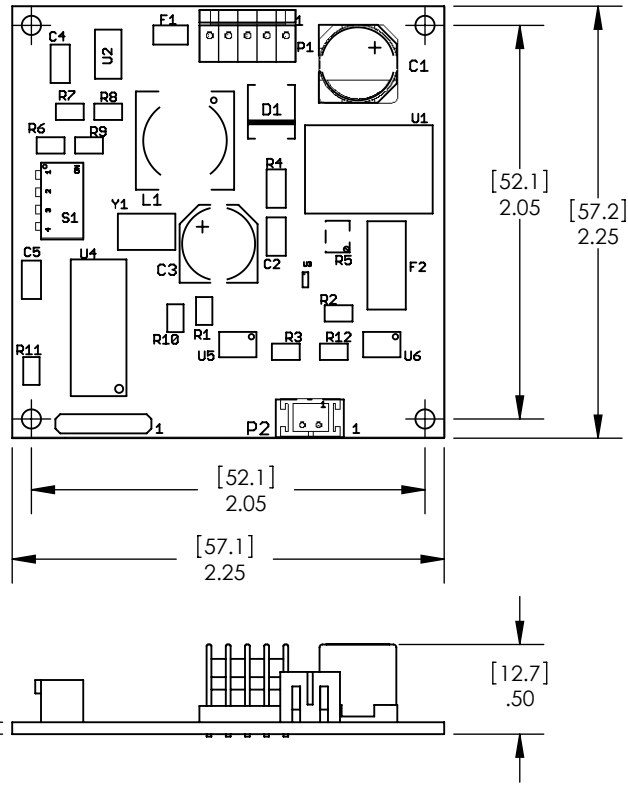
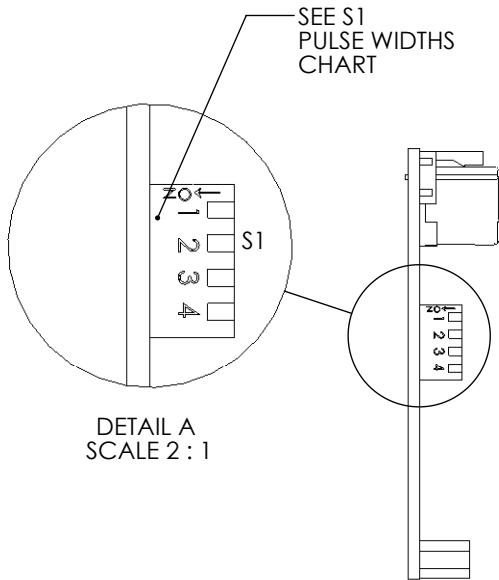


REVISIONS					
DATE	REV.	DESCRIPTION	DR	APPR.	CK
09/09/2009	A	INITIAL RELEASE	HY	JFP	AFL
11/4/2009	B	TITLE WAS DSS DRIVER, ADDED F1 AND F2, NOTE 3	DFU	JFP	AFL
1/11/2010	C	ADDED NOTE 4. DIM .50	HY	JFP	JFP
10/21/2010	D	ADDED SHUTTER SETTING COLUMN IN CHART	DFU	JFP	JFP



NOTES:

- P1 CONNECTIONS:  
 INPUT - PIN 1: +12 ~ 24VDC REG.  
 PASSIVE - PIN 2: POWER GND  
 PASSIVE - PIN 3: SIGNAL GND  
 INPUT - PIN 4: TRIGGER INPUT (ACTIVE HIGH)  
 OUTPUT - PIN 5: +5VDC, .25A MAX.
- P2 CONNECTIONS:  
 OUTPUT - PIN 1: SHUTTER (A)  
 OUTPUT - PIN 2: SHUTTER (B)
- F1 IS A .25A F-A SMT FUSE  
 F2 IS A .75A S-B SMT FUSE
- OVERALL HEIGHT APPROXIMATELY .750 WITH INPUT HARNESS CONNECTED.
- ALL DIMENSIONS MAXIMUM  
 [MM]  
 INCH

S1 (OPEN/CLOSE) PULSE WIDTHS					
1	2	3	4	TIME SELECT	CORRESPONDING SHUTTER SETTING
0	0	0	N/A	5msec	TS6
1	0	0	N/A	10msec	DSS10, NS15B
0	1	0	N/A	15msec	NS25B
1	1	0	N/A	20msec	
0	0	1	N/A	25msec	DSS20
1	0	1	N/A	30msec	NS35B, NS45B
0	1	1	N/A	35msec	DSS25
1	1	1	N/A	40msec	

1 = ON POSITION (DOWN)  
 0 = OFF POSITION (UP)

UNLESS OTHERWISE SPECIFIED:		NAME	DATE	VINCENT ASSOCIATES Rochester, NY 14625	VINCENT ASSOCIATES*
DIMENSIONS ARE IN INCHES		DRAWN	H. YAN		
TOLERANCES:		CHECKED	AFL	10/8/09	TITLE: ED12DSS DRIVER PCB OUTLINE DIAGRAM
FRACTIONAL ± 1/32		ENG. APPR.	JFP	10/8/09	
ANGULAR: MACH ± 1° BEND ± 1°		MATERIAL:		N/A	
TWO PLACE DECIMAL ± .01		Project: Drivers_DSS		SIZE	DWG. NO.
THREE PLACE DECIMAL ± .005		Model Name: DSS drive pcb Assy		A	16-0618
PRIMARY FINISH: REMOVE ALL BURRS BREAK SHARP EDGES		Drawing Name: DSS drive pcb outline		SCALE: 1:1	WEIGHT:
SECONDARY FINISH: N/A		DO NOT SCALE DRAWING		SHEET 1 OF 1	REV. D