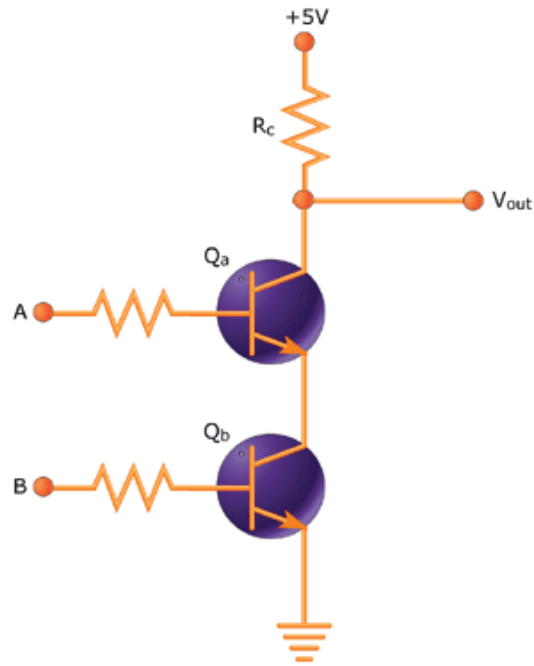
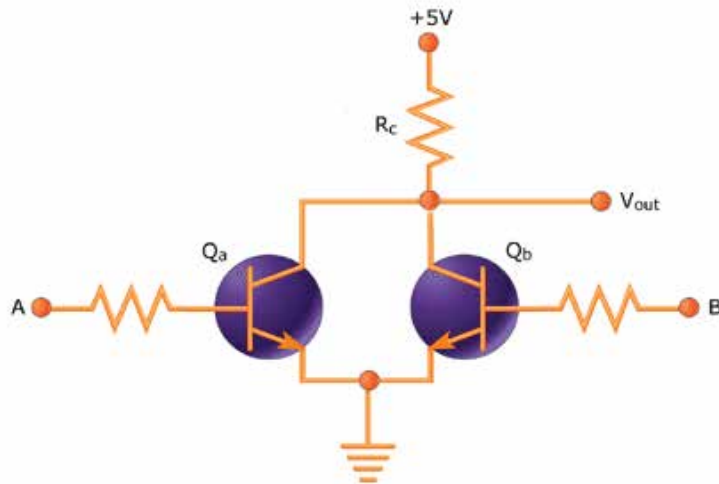


Fundamental transistor logic gates

NAND



NOR



NOT

