



High Voltage - High Current OPERATIONAL AMPLIFIER

FEATURES

- HIGH OUTPUT SWINGS. Up to $\pm 140V$
- LARGE LOAD CURRENTS. $\pm 75mA$
- PROTECTED OUTPUT STAGE. Automatic Thermal Shutoff
- REDUCES SOURCE LOADING. $10^{11} \Omega$ Input Z
- PRESERVES SYSTEM ACCURACY. 110dB CMR 20pA Bias Current

APPLICATIONS

- PROGRAMMABLE POWER SUPPLY OUTPUT AMPLIFIER
- HIGH VOLTAGE CURRENT SOURCE
- POWER BOOSTER
- HIGH VOLTAGE INTEGRATOR
- DIFFERENTIAL AMPLIFIER FOR HIGH COMMON-MODE VOLTAGE CIRCUITS

DESCRIPTION

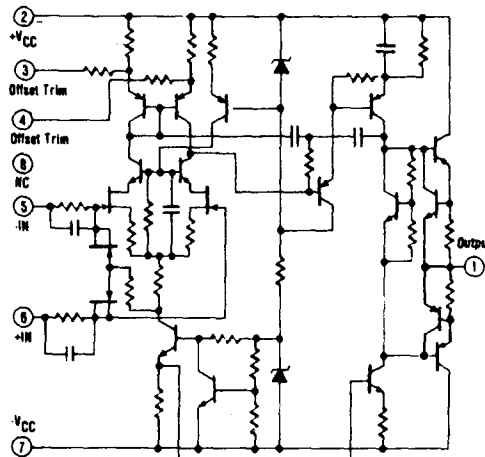
The 3583 is the first integrated circuit operational amplifier to provide output voltage swings of $\pm 140V$ with currents as high as $\pm 75mA$.

The amplifier operates over a wide supply range ($\pm 50VDC$ to $\pm 150VDC$) and has excellent input characteristics (110dB CMR, $3mV V_{os}$, $25\mu V/^\circ C \Delta V_{os} \Delta T$).

The monolithic FET input stage has low bias current (20pA) which minimizes the offset voltages caused by the bias current and the large resistances normally associated with high voltage circuits.

The input stage is protected against overvoltages and the output stage is protected against short-circuits to ground for supply voltages below $\pm 100VDC$. A special thermal sensing circuit prevents damage to the amplifier by automatically shutting the amplifier down when too much power is being dissipated.

Two temperature ranges are available: $0^\circ C$ to $+70^\circ C$ (3583JM) and $-25^\circ C$ to $+85^\circ C$ (3583AM).



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PDS-343D

SPECIFICATIONS

ELECTRICAL

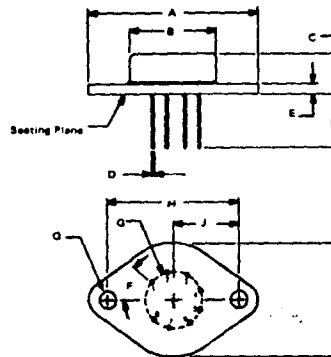
Specifications typical at T_{case} = +25°C and ±V_{cc} = 150VDC unless otherwise noted.

MODELS	3583AM	3583JM
POWER SUPPLY		
Voltage, ±V _{cc}	±50VDC to ±150VDC	
Quiescent Current, max	8.5mA	
RATED OUTPUT		
Voltage, ± V _{cc} -10 VDC, min	±40VDC to ±140VDC	
Current, min	±75mA	
Current, Short Circuit	±100mA	
Load Capacitance, max	10nF	
OPEN-LOOP GAIN		
No Load, DC	118dB	
Rated Load, DC	94dB, min; 105dB, typ	
FREQUENCY RESPONSE		
Unity Gain Bandwidth, Small Signal	5MHz	
Full Power Bandwidth, R _L = 10kΩ	60kHz	
Slew Rate	30V/μsec	
Settling Time, 0.1%	12μsec	
INPUT OFFSET VOLTAGE T _A = +25°C		
Initial at 25°C, max	±3mV	
Drift vs Temp, max	±23μV/°C	
Drift vs Supply Voltage	±20μV/V	
Drift vs Time	±50μV/mo	
INPUT BIAS CURRENT		
Initial at 25°C, max	-20pA	
Drift vs Temp	doubles every 10°C	
Drift vs Supply Voltage	0.2pA/V	
INPUT OFFSET CURRENT		
Initial at 25°C	±20pA	
Drift vs Temp	doubles every 10°C	
Drift vs Supply Voltage	0.2pA/V	
INPUT IMPEDANCE		
Differential	10 ¹¹ Ω 10pF	
Common-mode	10 ¹¹ Ω	
INPUT NOISE		
Voltage 0.01Hz to 10Hz, p-p	5μV	
10Hz to 1kHz, rms	1.7μV	
Current 0.01Hz to 10Hz, p-p	0.3pA	
INPUT VOLTAGE RANGE		
Max Safe Differential Voltage ⁽¹⁾	(+V _{cc} + -V _{cc})	
Max Safe Common-mode Voltage	+V _{cc} to -V _{cc}	
Common-mode Voltage, Linear Operation	± V _{cc} -10V	
Common-mode Rejection	110dB	
TEMPERATURE RANGE (Case)		
Specification	-25°C to +85°C 0°C to 70°C	
Operating	-55°C to +125°C	
Storage	-55°C to +125°C	

NOTES:

- The inputs may be damaged by pulses at pins 5 or 8 with dV/dt ≥ 1V/nsec. Any possible damage can be eliminated by limiting the input current to 150mA with external resistors in series with those pins. No external protection is needed for slower voltage changes.

MECHANICAL



NOTE:
Leads in true section within .010" (1.28mm) R @ MMC or seating plane.

Pin numbers shown for reference only. Numbers may not be marked on package.

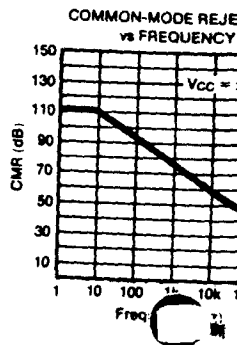
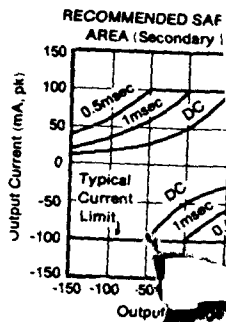
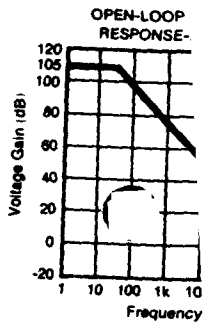
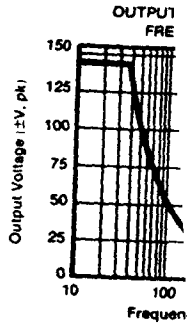
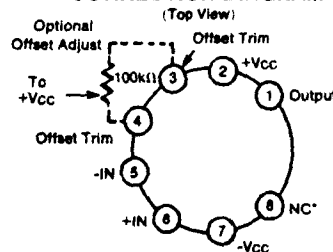
ORDER NUMBER:
3583AM 3583JM

WEIGHT:
15.1 Grams

MATING CONNECTOR:
0803MC

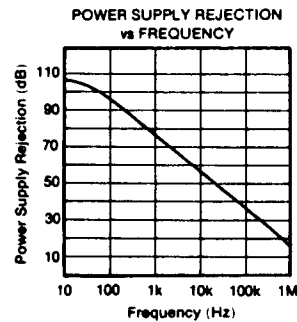
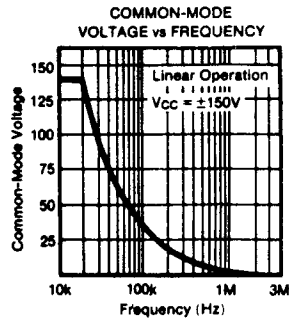
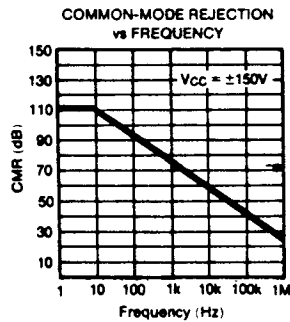
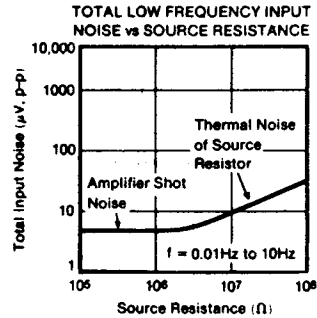
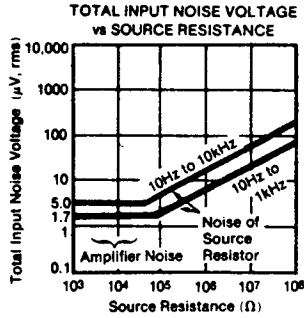
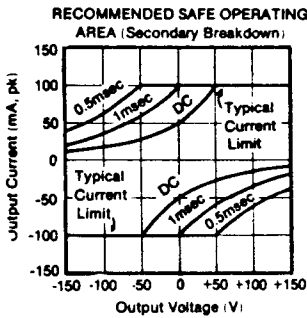
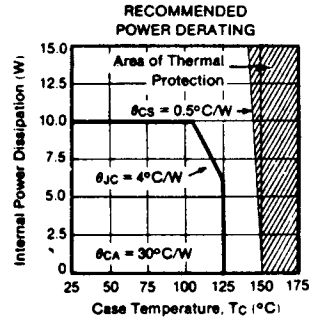
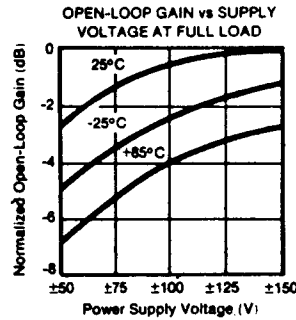
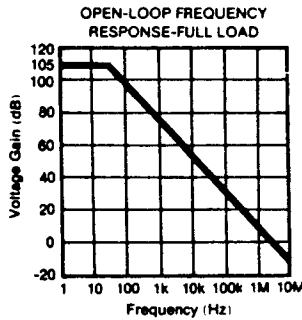
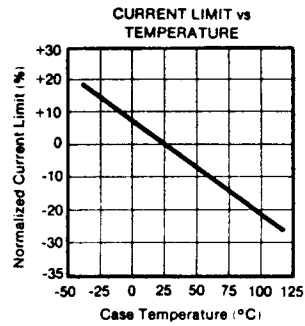
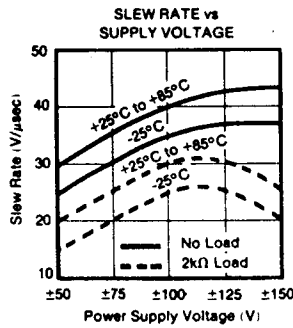
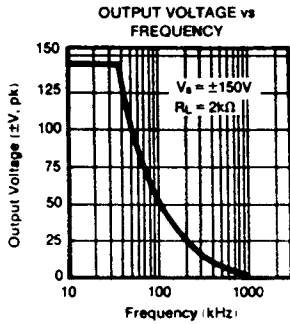
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	1.510	1.560	38.35	39.37
B	.745	.770	18.92	19.56
C	.280	.300	6.90	7.62
D	.038	.042	0.97	1.07
E	.080	.105	2.03	2.67
F	40° BASIC		40° BASIC	
G	500 BASIC		12.7 BASIC	
H	1.186 BASIC		30.12 BASIC	
J	583 BASIC		15.08 BASIC	
K	.400	.500	10.16	12.70
Q	.151	.161	3.84	4.09
R	.960	1.020	24.99	25.91

CONNECTION DIAGRAM



TYPICAL PERFORMANCE CURVES

Typical at $T_{CASE} = +25^{\circ}C$ and $\pm V_{CC} = 150VDC$ unless otherwise noted.



3583

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OPERATIONAL AMPLIFIERS

APPLICATIONS INFORMATION

The 3583 is a high voltage, high output current integrated circuit operational amplifier. Its ease of use, compact size, and excellent input and output specifications makes it well suited for a wide variety of high voltage applications.

The equivalent circuit for the 3583 is shown in Figure 1. The design uses a monolithic FET input stage for high input impedance, low bias current, and low voltage drift versus temperature. The offset voltage at 25°C and the drift versus temperature are compensated by state-of-the-art laser-trimming techniques. They are low enough so that user-trimming will not be required in most applications. The high input impedance provides negligible source impedance loading errors when the noninverting circuit configuration is used. The low bias currents minimize offset errors when large values of source and feedback resistors are used.

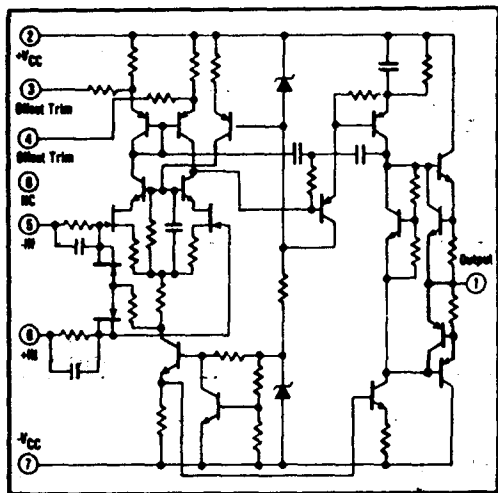


FIGURE 1. 3583 Equivalent Circuit.

A true cascade input stage is used together with considerable protection circuitry. There are voltage limiting transistors to prevent damage due to reverse bias breakdown of the input pair and current limiting resistors to limit the input current to 1mA with the inputs at ± 150 volts. The units are conservatively rated (and 100% tested) at full rated differential voltage ($+150\text{V}$ and -150V) but typically will withstand a 50% overvoltage without damage.

The unit operates over a wide supply range ($\pm 50\text{V}$ to $\pm 150\text{V}$) with outstanding common-mode rejection (110dB). It also has another feature which is important in many high voltage applications. The input bias current is virtually independent of applied common-mode voltage. The output circuit has a unique protection feature which is only practical in integrated-circuit amplifiers - self-contained automatic thermal sensing and shutoff circuitry which automatically turns the amplifier off when the internal temperature reaches approximately 150°C. This is accomplished by sensing the substrate temperature and deactivating the amplifier's biasing network when the temperature reaches 150°C. As this happens, the output load current limits at a safe value and the amplifier's quiescent current decreases. The output current will remain at a low value or oscillate between two values depending on the amount of power being dissipated and the heat sink conditions seen by the amplifier. In either case, the amplifier will not sustain internal damage and will return to normal operation within a few seconds after the abnormal load condition is removed.

The internal thermal protection removes some of the constraints of power derating for abnormal operating conditions. The amplifier will protect itself for many conditions of excess power dissipation (see the Power Derating Curve). This allows the use of a smaller heat sink to protect against abnormal output conditions since the amplifier has its own internal protection for many conditions of excess power dissipation. The output constraints of the Recommended Safe Operating Area curves must still be observed.

The 3583 has several other features that improve its utility. For instance, the metal case of the unit is completely electrically isolated. (This can be contrasted to most power semiconductors where the case is connected to the collector of the device.) This simplifies mounting and reduces cost since the need for insulating spacers and bushings is eliminated. The hermetically sealed package improves reliability and will more easily withstand severe environments than do discrete component amplifiers. The small package size reduces weight and makes mounting more convenient.

Burr-Brown offers three heat sinks as accessories: 0803HS with a thermal resistance of 12°C/watt, 0804HS at 4.2°C/watt, and 0805HS at 3°C/watt. A convenient mating connector, 0803MC is also available.

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