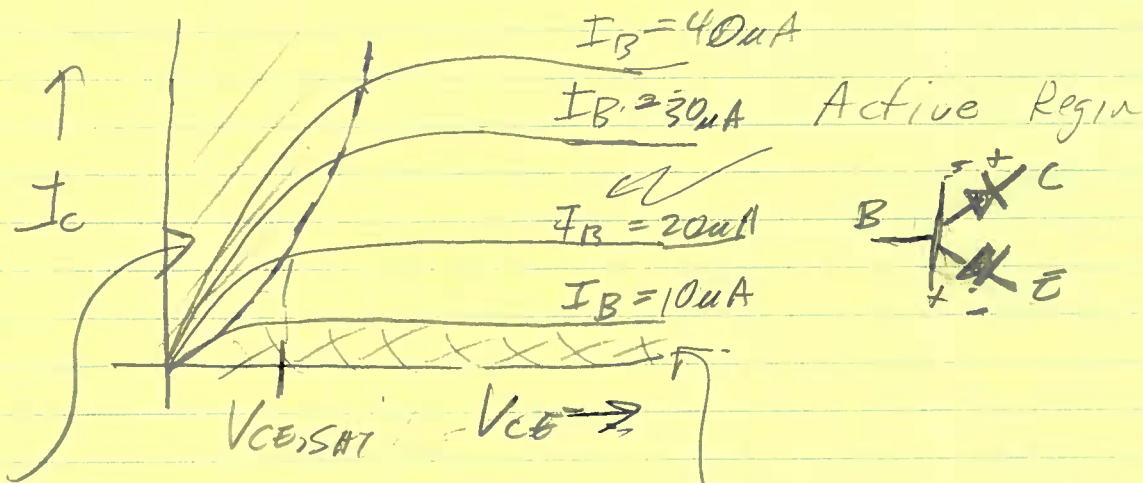


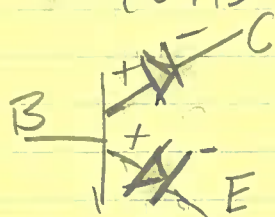
①

4 June 2013

Back to Transistors!

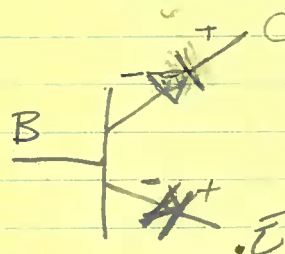


Saturation Region (ON)

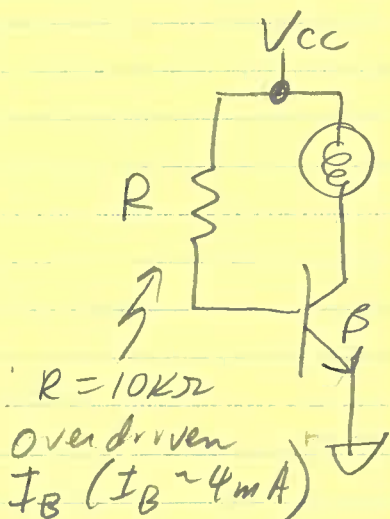


Both Forward biased
High conduction
Note: $I_C \neq \beta I_B$

Cut off Region (OFF)



Both Reverse biased
No conduction



$$\frac{V_{CC} - V_{BE}}{R} = I_B$$

$$\frac{V_{CC} - V_{CE}}{R_L} = I_C$$

Is $I_C > \beta I_B$?

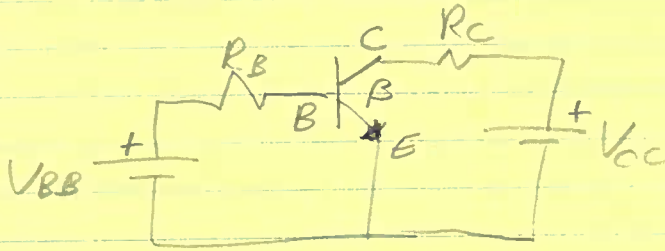
small (from tables)

$$\frac{V_{CC} - V_{CE}}{R_L} > \beta \frac{V_{CC} - V_{BE}}{R}$$

$\sim 120mA$ $\sim 4mA$

(2)

Example of a common emitter circuit.



$$\begin{aligned}
 V_{CC} &= +10V \\
 R_C &= 1k\Omega \\
 R_B &= 10k\Omega \\
 \beta &= 100
 \end{aligned}$$

$$-V_{BB} + I_B R_B + V_{BE} = 0$$

$$\textcircled{1} \quad I_B = \frac{V_{BB} - V_{BE}}{R_B} \quad ; \quad V_{BE} \approx 0.7V$$

$$-V_{CC} + I_C R_C + V_{CE} = 0$$

$$\textcircled{2} \quad V_{CE} = V_{CC} - I_C R_C \quad \text{or} \quad I_C = \frac{V_{CC} - V_{CE}}{R_C}$$

Case of $V_{BB} = +1.5V$.

$$I_B = 80 \mu A$$

$$I_C = \beta I_B = 8mA$$

$$V_{CE} = 2V \rightarrow V_{CE, SAT} !$$

Case of $V_{BB} = 10.7V$

$$I_B = 1mA$$

$$I_C = 100mA$$

$$V_{CE} = -90V$$

} Impossible as $V_{CE} < V_{CE, SAT}$
So linear analysis does not hold.

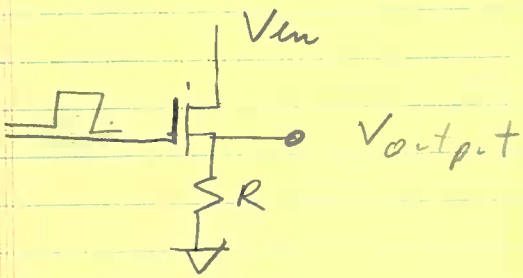
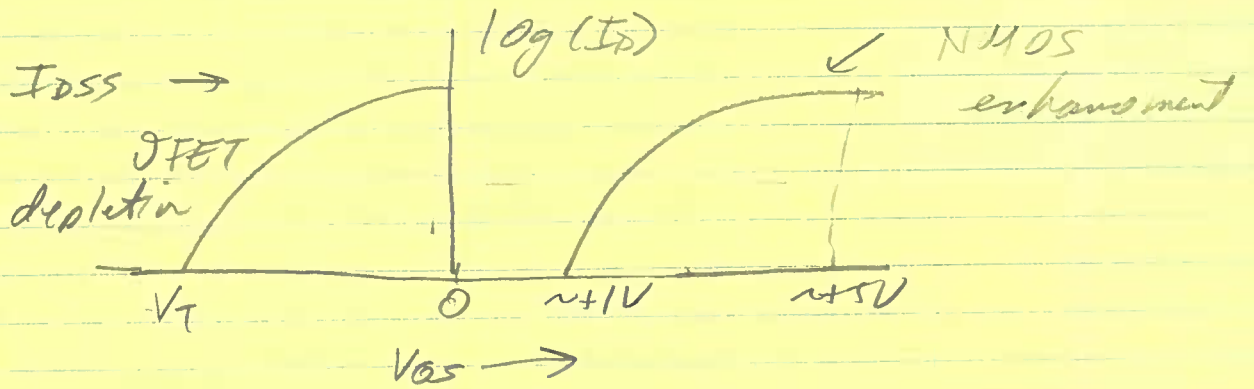
$$V_{CE} = V_{CE, SAT} = 0.1V$$

$$I_C = \frac{V_{CC} - V_{CE, SAT}}{R_C} = 9.9mA \quad \left. \begin{array}{l} \\ \end{array} \right\} \beta \approx 10$$

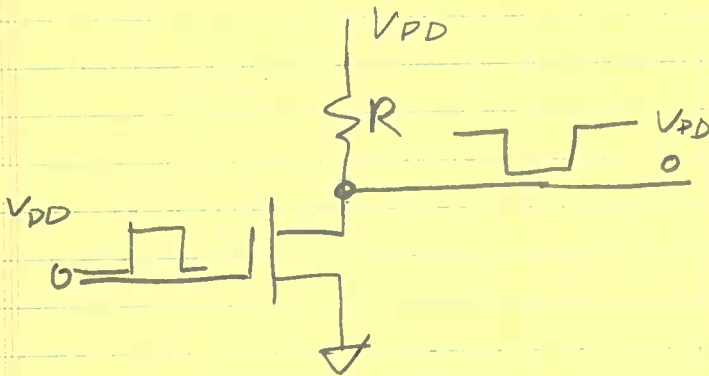
$$I_E = I_C + I_B = 10.9mA$$

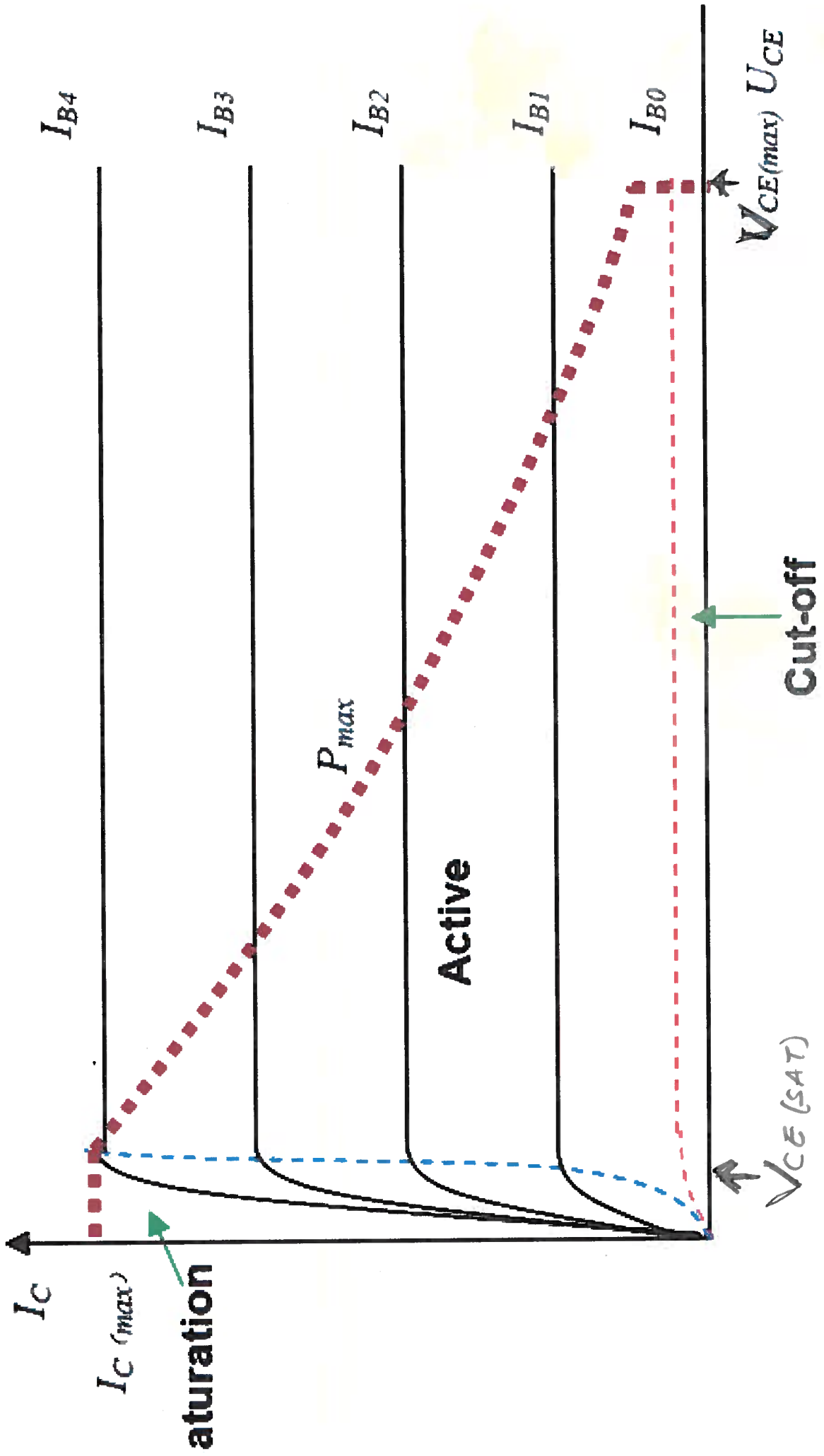
} High current with reduced gain

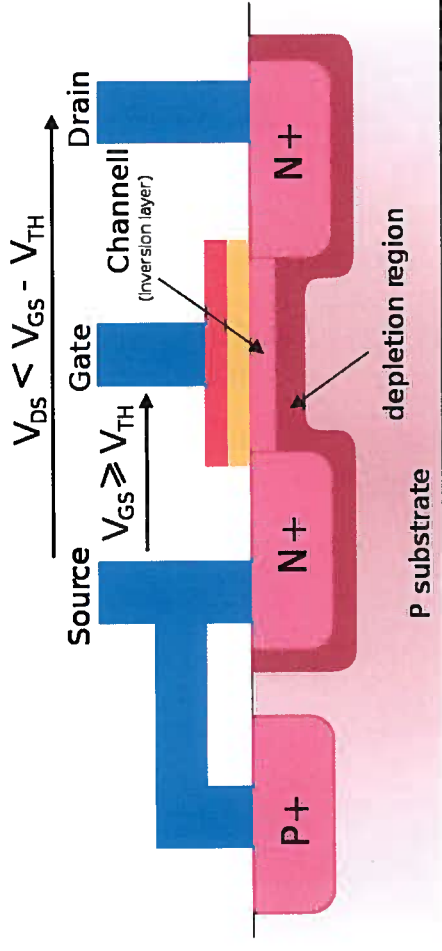
3



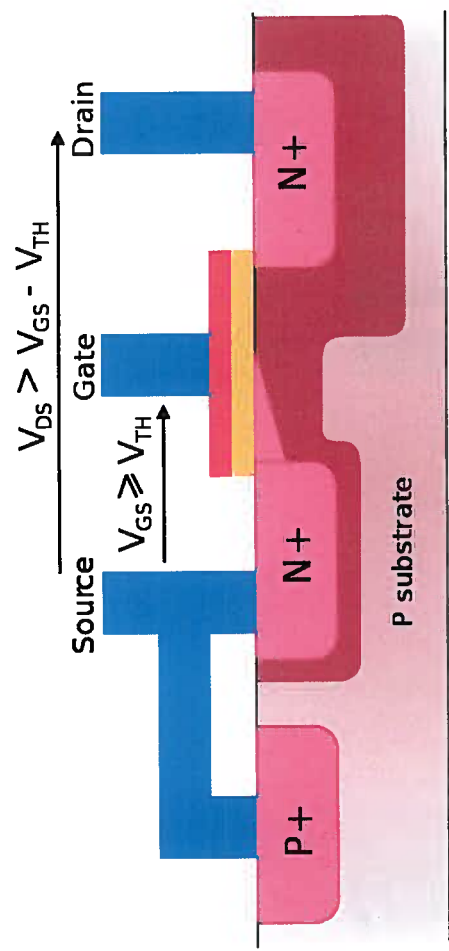
Inversion-Critical for logic



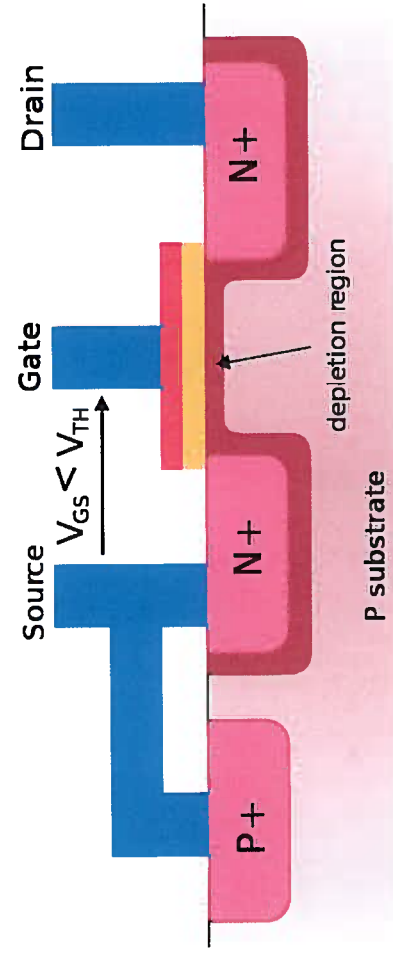




Linear operating region (ohmic mode)



Saturation mode



Saturation mode at point of pinch-off

