

# LM124, LM124A, LM224, LM224A, LM324, LM324A, LM2902, LM2902V, LM224K, LM224KA, LM324K, LM324KA, LM2902K, LM2902KV, LM2902KAV QUADRUPLE OPERATIONAL AMPLIFIERS

SLOS066R – SEPTEMBER 1975 – REVISED JANUARY 2005

- **2-kV ESD Protection for:**
  - LM224K, LM224KA
  - LM324K, LM324KA
  - LM2902K, LM2902KV, LM2902KAV
- **Wide Supply Ranges**
  - Single Supply . . . 3 V to 32 V  
(26 V for LM2902)
  - Dual Supplies . . .  $\pm 1.5$  V to  $\pm 16$  V  
( $\pm 13$  V for LM2902)
- **Low Supply-Current Drain Independent of Supply Voltage . . . 0.8 mA Typ**
- **Common-Mode Input Voltage Range Includes Ground, Allowing Direct Sensing Near Ground**
- **Low Input Bias and Offset Parameters**
  - Input Offset Voltage . . . 3 mV Typ  
A Versions . . . 2 mV Typ
  - Input Offset Current . . . 2 nA Typ
  - Input Bias Current . . . 20 nA Typ  
A Versions . . . 15 nA Typ
- **Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . 32 V (26 V for LM2902)**
- **Open-Loop Differential Voltage Amplification . . . 100 V/mV Typ**
- **Internal Frequency Compensation**

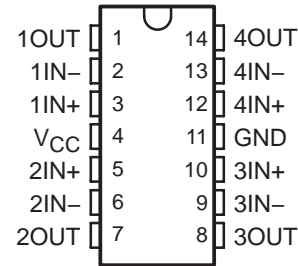
## description/ordering information

These devices consist of four independent high-gain frequency-compensated operational amplifiers that are designed specifically to operate from a single supply over a wide range of voltages. Operation from split supplies also is possible if the difference between the two supplies is 3 V to 32 V (3 V to 26 V for the LM2902), and  $V_{CC}$  is at least 1.5 V more positive than the input common-mode voltage. The low supply-current drain is independent of the magnitude of the supply voltage.

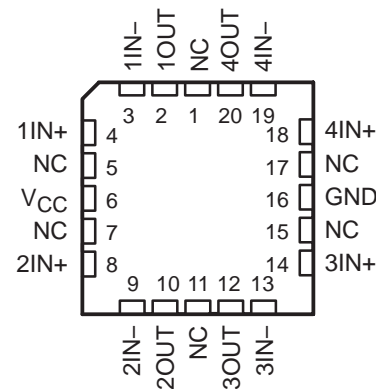
Applications include transducer amplifiers, dc amplification blocks, and all the conventional operational-amplifier circuits that now can be more easily implemented in single-supply-voltage systems. For example, the LM124 can be operated directly from the standard 5-V supply that is used in digital systems and provides the required interface electronics, without requiring additional  $\pm 15$ -V supplies.

LM124 . . . D, J, OR W PACKAGE  
LM124A . . . J PACKAGE  
LM224, LM224A, LM224K, LM224KA . . . D OR N PACKAGE  
LM324, LM324K . . . D, N, NS, OR PW PACKAGE  
LM324A . . . D, DB, N, NS, OR PW PACKAGE  
LM324KA . . . D, N, NS, OR PW PACKAGE  
LM2902 . . . D, N, NS, OR PW PACKAGE  
LM2902K . . . D, DB, N, NS, OR PW PACKAGE  
LM2902KV, LM2902KAV . . . D OR PW PACKAGE

(TOP VIEW)



LM124, LM124A . . . FK PACKAGE  
(TOP VIEW)

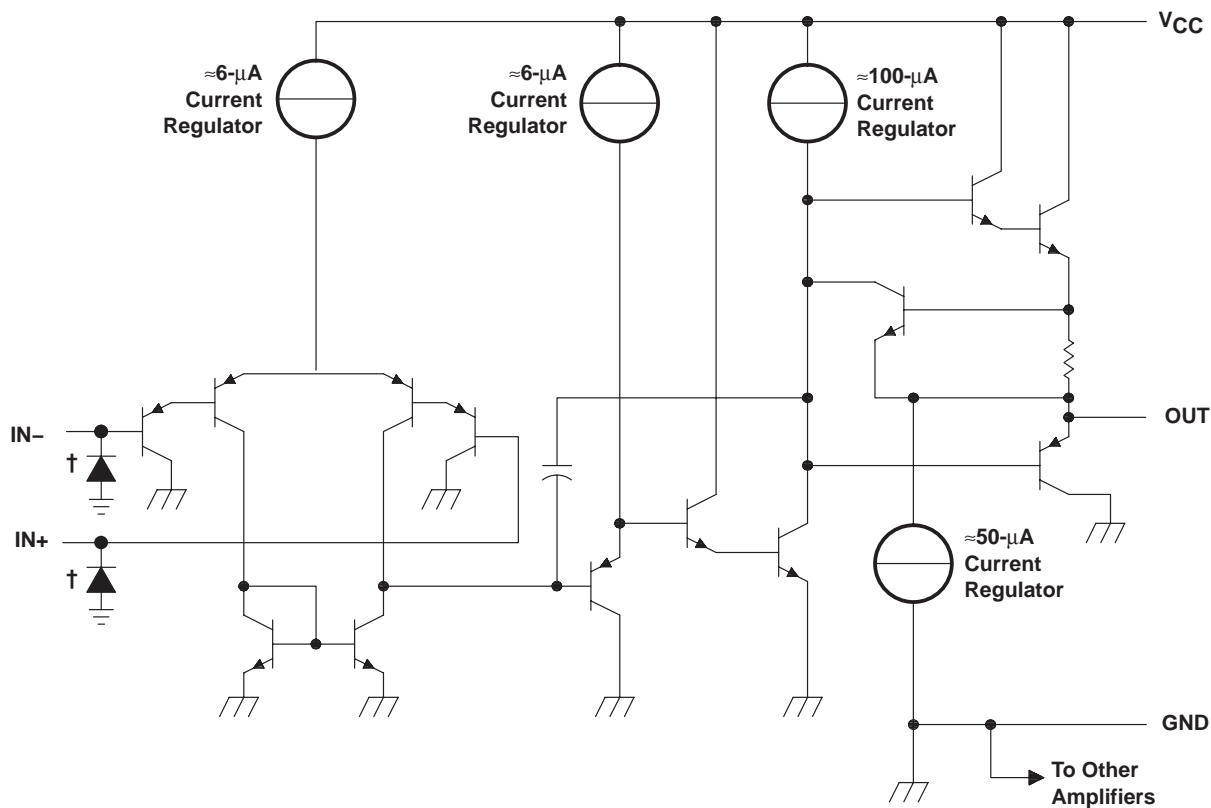


NC – No internal connection

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**schematic (each amplifier)**



COMPONENT COUNT (total device)	
Epi-FET	1
Transistors	95
Diodes	4
Resistors	11
Capacitors	4

† ESD protection cells - available on LM324K and LM324KA only



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electrical characteristics at specified free-air temperature,  $V_{CC} = 5\text{ V}$  (unless otherwise noted)

PARAMETER	TEST CONDITIONS†	$T_A$ ‡	LM124 LM224			LM324 LM324K			UNIT		
			MIN	TYP§	MAX	MIN	TYP§	MAX			
$V_{IO}$ Input offset voltage	$V_{CC} = 5\text{ V to MAX}$ , $V_{IC} = V_{ICRmin}$ , $V_O = 1.4\text{ V}$	25°C		3	5		3	7	mV		
		Full range						9			
$I_{IO}$ Input offset current	$V_O = 1.4\text{ V}$	25°C		2	30		2	50	nA		
		Full range			100			150			
$I_{IB}$ Input bias current	$V_O = 1.4\text{ V}$	25°C		-20	-150		-20	-250	nA		
		Full range			-300			-500			
$V_{ICR}$ Common-mode input voltage range	$V_{CC} = 5\text{ V to MAX}$	25°C		0 to $V_{CC} - 1.5$		0 to $V_{CC} - 1.5$			V		
		Full range		0 to $V_{CC} - 2$		0 to $V_{CC} - 2$					
$V_{OH}$ High-level output voltage	$R_L = 2\text{ k}\Omega$	25°C		$V_{CC} - 1.5$			$V_{CC} - 1.5$			V	
		25°C									
	$V_{CC} = \text{MAX}$	$R_L = 2\text{ k}\Omega$	Full range		26		26				
		$R_L \geq 10\text{ k}\Omega$	Full range		27	28	27	28			
$V_{OL}$ Low-level output voltage	$R_L \leq 10\text{ k}\Omega$	Full range		5	20		5	20	mV		
$A_{VD}$ Large-signal differential voltage amplification	$V_{CC} = 15\text{ V}$ , $V_O = 1\text{ V to }11\text{ V}$ , $R_L \geq 2\text{ k}\Omega$	25°C		50	100		25	100	V/mV		
		Full range		25			15				
CMRR Common-mode rejection ratio	$V_{IC} = V_{ICRmin}$	25°C		70	80		65	80	dB		
$k_{SVR}$ Supply-voltage rejection ratio ( $\Delta V_{CC}/\Delta V_{IO}$ )		25°C		65	100		65	100	dB		
$V_{O1}/V_{O2}$ Crosstalk attenuation	$f = 1\text{ kHz to }20\text{ kHz}$	25°C		120			120			dB	
$I_O$ Output current	$V_{CC} = 15\text{ V}$ , $V_{ID} = 1\text{ V}$ , $V_O = 0$	Source	25°C		-20	-30	-60	-20	-30	-60	mA
			Full range			-10			-10		
	$V_{CC} = 15\text{ V}$ , $V_{ID} = -1\text{ V}$ , $V_O = 15\text{ V}$	Sink	25°C		10	20		10	20		
			Full range		5			5			
	$V_{ID} = -1\text{ V}$ , $V_O = 200\text{ mV}$	25°C		12	30		12	30	$\mu\text{A}$		
$I_{OS}$ Short-circuit output current	$V_{CC}$ at 5 V, $V_O = 0$ , GND at -5 V	25°C		$\pm 40$	$\pm 60$		$\pm 40$	$\pm 60$	mA		
$I_{CC}$ Supply current (four amplifiers)	$V_O = 2.5\text{ V}$ , No load	Full range		0.7	1.2		0.7	1.2	mA		
	$V_{CC} = \text{MAX}$ , $V_O = 0.5 V_{CC}$ , No load	Full range		1.4	3		1.4	3			

† All characteristics are measured under open-loop conditions, with zero common-mode input voltage, unless otherwise specified. MAX  $V_{CC}$  for testing purposes is 26 V for LM2902 and 30 V for the others.

‡ Full range is -55°C to 125°C for LM124, -25°C to 85°C for LM224, and 0°C to 70°C for LM324.

§ All typical values are at  $T_A = 25^\circ\text{C}$ .



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operating conditions,  $V_{CC} = \pm 15\text{ V}$ ,  $T_A = 25^\circ\text{C}$

PARAMETER		TEST CONDITIONS	TYP	UNIT
SR	Slew rate at unity gain	$R_L = 1\text{ M}\Omega$ , $C_L = 30\text{ pF}$ , $V_I = \pm 10\text{ V}$ (see Figure 1)	0.5	$\text{V}/\mu\text{s}$
$B_1$	Unity-gain bandwidth	$R_L = 1\text{ M}\Omega$ , $C_L = 20\text{ pF}$ (see Figure 1)	1.2	MHz
$V_n$	Equivalent input noise voltage	$R_S = 100\ \Omega$ , $V_I = 0\text{ V}$ , $f = 1\text{ kHz}$ (see Figure 2)	35	$\text{nV}/\sqrt{\text{Hz}}$

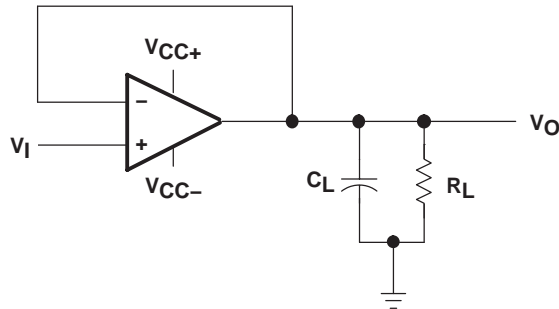


Figure 1. Unity-Gain Amplifier

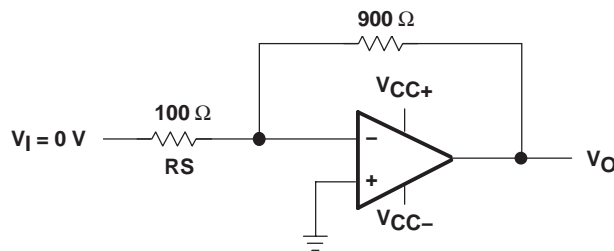


Figure 2. Noise-Test Circuit